

**CENTRAL NEW MEXICO COMMUNITY COLLEGE**  
**ASSESSMENT REPORT-Part I**  
**Assessment Data Results**

The purpose of this form is to provide a written summary of your assessment results for the current assessment cycle.

2012-2013  
 (Assessment Period Covered)

6-13-13  
 (Date Report Submitted)

Andy Huertaz/Andy@cnm.edu/50189  
 (Contact Person/email/phone)

**Choose ONE of the following 3 areas for this assessment report and insert the name of the general education area, certificate, degree or discipline on the appropriate line:**

**See definitions for each category in Assessment Process document**

<b>Gen Ed Area</b> (see definitions) _____  AA/AS <input type="checkbox"/> AAS <input type="checkbox"/>	or	<b>Program</b> <u>Advanced Systems Tech</u>  Certificate <input type="checkbox"/> AA/AS <input type="checkbox"/> AAS <input checked="" type="checkbox"/>
<b>Or Discipline Area</b> (see definitions) _____		
Outcome(s) assessed: <ul style="list-style-type: none"> <li>• Analyze, design and troubleshoot basic analog and digital electronics circuits with emphasis on circuit wiring and computer simulation.</li> <li>• Analyze, troubleshoot and repair electronics equipment without causing additional damage to the equipment or injury to self.</li> <li>• Design, analyze and troubleshoot basic electronics circuits using a variety of test equipment and tools.</li> </ul>		
Classes/Cohort Assessed: Capstone Elec 2999 Spring 2013		
Measurement tool(s):  Final Project		
Type of tool (for each tool listed above, indicate type of tool):  Direct - Project Design (software), Direct - fabrication(Hardware), Direct - presentation(Written and video).		

Achievement Target (if more than one measurement tool, list target for each tool separately):

Project design must include specification sheets,(25%) circuit design schematics, (25%) Presentation (25%), and final project. (25%)

Assessment Results/Findings (if more than one measurement tool, list results for each tool separately):

Specification sheets = 96% of students came up with documentation for components and fabrication.

Circuit Design Schematics = 96% students designed and laid-out their circuit design using Multisim® software

Presentations = included video presentations posted on the web, and written documentation of progress and final results.

Final Projects = 96% presented final projects.

**CENTRAL NEW MEXICO COMMUNITY COLLEGE  
ASSESSMENT REPORT – Part II  
Action Plan & Assessment Plan Update**

The purpose of this form is to provide a written summary of your assessment action plan for the designated assessment cycle and provide an updated assessment cycle plan for the current 5-year cycle

Fall 2012-13  
 \_\_\_\_\_  
 (Report Period)  
 Andy Huertaz/Andy@cnm.edu  
 \_\_\_\_\_  
 (Contact Person/email/phone)

9-15-13  
 \_\_\_\_\_  
 (Date Report Submitted)

Indicate **ONE** of the following **3** areas for this assessment report and insert the name of the general education area, certificate, degree or discipline on the appropriate line:

See definitions for each category in Assessment Process document

<b>Gen Ed Area</b> (see definitions) _____  AA/AS <input type="checkbox"/> AAS <input type="checkbox"/>	or	<b>Program</b> _____ Advanced Systems Technology  Certificate <input type="checkbox"/> AA/AS <input type="checkbox"/> AAS <input checked="" type="checkbox"/>
<b>Or Discipline Area</b> (see definitions) _____		

Data Results Period upon which this Action Plan is based (period which ended 6/30/14):

- Accurately verbalize terminology and concepts associated with electronics circuits – 100%
- Analyze, design, and troubleshoot basic analog and digital electronics circuits with emphasis on circuit wiring and computer simulation. -83%
- Design and build working electromechanical systems within given parameters. 100%
- Apply knowledge of safety and troubleshooting techniques to systematically analyze and repair electronic systems using a variety of test equipment and tools. 83%
- Analyze, troubleshoot, repair electronics equipment without causing additional damage to the equipment or injury to self. 83%

- Design, analyze and troubleshoot basic electronics circuits using a variety of test equipment and tools. - 100%
- Apply a systematic approach to problem-solving when troubleshooting and repairing an industrial system. –NA (Not assessed)

**Action Plan (close the loop):**

To continue assessment of student outcomes during the capstone course and to include a measurement tool for the industrial system troubleshooting and repair.

**ASSESEMENT PLAN**

**The assessment plan includes three parts:**

- 1. The plan description** (This should be a brief written description of the assessment plan(s) for the area/certificate/degree/discipline. If all outcomes are not shown in item #3 below as assessed in the 5 year cycle, this description must include information about their eventual assessment)
- 2. The student learning outcomes for the area/program/discipline** for the 5 year cycle.
- 3. The assessment cycle timeline**

**1 Plan Description**

**To assess all student outcomes during the final course in the AST program. (Capstone)**

- 2 Provide the list of current student learning outcomes for this area or program (you may add more lines if necessary by right clicking and choosing insert row below):**

1	<ul style="list-style-type: none"> <li>• Accurately verbalize terminology and concepts associated with electronics circuits</li> </ul>
2	<ul style="list-style-type: none"> <li>• Analyze, design, and troubleshoot basic analog and digital electronics circuits with emphasis on circuit wiring and computer simulation.</li> </ul>
3	<ul style="list-style-type: none"> <li>• Design and build working electromechanical systems within given parameters.</li> </ul>

4	<ul style="list-style-type: none"> <li>Apply knowledge of safety and troubleshooting techniques to systematically analyze and repair electronic systems using a variety of test equipment and tools.</li> </ul>
5	<ul style="list-style-type: none"> <li>Analyze, troubleshoot, repair electronics equipment without causing additional damage to the equipment or injury to self.</li> </ul>
6	<ul style="list-style-type: none"> <li>Design, analyze and troubleshoot basic electronics circuits using a variety of test equipment and tools.</li> </ul>
7	<ul style="list-style-type: none"> <li>Apply a systematic approach to problem-solving when troubleshooting and repairing an industrial system.</li> </ul>
8	
9	
10	

**3 Assessment Cycle timeline for the above student learning outcomes for the next five years.**

<b>Outcome #</b>	<b>When Measured</b>	<b>Where measured (i.e. what course(s))</b>	<b>Measurement tool(s) &amp; Type of tool</b>
1	Fall, Spring, Summer	Capstone	Final Project - direct
2	Fall, Spring, Summer	Capstone	Final Project - direct
3	Fall, Spring, Summer	Capstone	Final Project - direct
4	Fall, Spring, Summer	Capstone	Final Project - direct
5	Fall, Spring, Summer	Capstone	Final Project - direct
6	Fall, Spring, Summer	Capstone	Final Project - direct
7	Fall, Spring, Summer	Capstone	Final Project - direct
8			
9			
10			