

**CENTRAL NEW MEXICO COMMUNITY COLLEGE**  
**ASSESSMENT REPORT-Part I**  
**Assessment Data Results**

The purpose of this form is to provide a written summary of your assessment results for the current assessment cycle.

Fall 2011 and Spring 2012  
 (Assessment Period Covered)

6-15-2012  
 (Date Report Submitted)

Andy Huertaz / Andy@cnm.edu /224-4000 ext- 0189  
 (Contact Person/email/phone)

**Choose ONE of the following 3 areas for this assessment report and insert the name of the general education area, certificate, degree or discipline on the appropriate line:**

**See definitions for each category in Assessment Process document**

<b>Gen Ed Area</b> (see definitions) _____  AA/AS <input type="checkbox"/> AAS <input type="checkbox"/>	or	<b>Program</b> _____  Certificate AA/AS AAS	<b>Electronics</b> _____  <input type="checkbox"/> <input type="checkbox"/> <input checked="" type="checkbox"/>
<b>Or Discipline Area</b> (see definitions) _____			
Outcome(s) assessed: <ul style="list-style-type: none"> <li>• Analyze, design and troubleshoot basic analog and digital electronics circuits with emphasis on circuit wiring and computer simulation.</li> <li>• Analyze, troubleshoot and repair electronics equipment without causing additional damage to the equipment or injury to self.</li> <li>• Design, analyze and troubleshoot basic electronics circuits using a variety of test equipment and tools.</li> </ul>			
Classes/Cohort Assessed: Capstone Elec 2999 Spring 2012			
Measurement tool(s): Final Project			
Type of tool (for each tool listed above, indicate type of tool): Direct - Project Design (software), Direct - fabrication(Hardware), Direct - presentation(Written and video).			

Achievement Target (if more than one measurement tool, list target for each tool separately):

Project design must include specification sheets,(25%) circuit design schematics, (25%) Presentation (25%), and final project. (25%)

Assessment Results/Findings (if more than one measurement tool, list results for each tool separately):

Specification sheets = 100% of students came up with documentation for components and fabrication.

Circuit Design Schematics = 100% students designed and laid-out their circuit design using Multisim® software

Presentations = included video presentations posted on the web, and written documentation of progress and final results.

Final Projects = 100% presented final projects.

**CENTRAL NEW MEXICO COMMUNITY COLLEGE  
ASSESSMENT REPORT – Part II  
Action Plan & Assessment Plan Update**

The purpose of this form is to provide a written summary of your assessment action plan for the designated assessment cycle and provide an updated assessment cycle plan for the current 5-year cycle

Fall 2011 and Spring 2012  
 (Report Period)  
 Andy Huertaz / [Andy@cnm.edu](mailto:Andy@cnm.edu) / 224-4000 ext 0189  
 (Contact Person/email/phone)

6-15-2012  
 (Date Report Submitted)

**Indicate ONE of the following 3 areas for this assessment report and insert the name of the general education area, certificate, degree or discipline on the appropriate line:**

See definitions for each category in Assessment Process document

<b>Gen Ed Area</b> (see definitions) _____  AA/AS <input type="checkbox"/> AAS <input type="checkbox"/>	or	<b>Program</b> <u>Electronics</u>  Certificate <input type="checkbox"/> AA/AS <input type="checkbox"/> AAS <input checked="" type="checkbox"/>
<b>Or Discipline Area</b> (see definitions) _____		
Data Results Period upon which this Action Plan is based (period which ended 6/30/xx): Fall 2011 and Spring 2012 Results show that all students achieved outcomes to a level substantial for degree status.		
Action Plan (close the loop): The plan is to maintain outcome status and to maintain updated outcomes with new technologies as they are demanded by local industry.		

**ASSESEMENT PLAN**

The assessment plan includes three parts:

1. **The plan description** (This should be a brief written description of the assessment plan(s) for the area/certificate/degree/discipline. If all outcomes are not shown in item #3 below as assessed in the 5 year cycle, this description must include information about their eventual assessment)
2. **The student learning outcomes for the area/program/discipline** for the 5 year cycle.
3. **The assessment cycle timeline**

**1 Plan Description**  
Continued use of capstone projects for students to demonstrate successful achievement of learning outcomes of the program, but as the program merges with Photonics and MEMS, new outcomes must be introduced.

**2 Provide the list of current student learning outcomes for this area or program (you may add more lines if necessary by right clicking and choosing insert row below):**

1	Analyze, design and troubleshoot basic analog and digital electronics circuits with emphasis on circuit wiring and computer simulation.
2	Analyze, troubleshoot and repair electronics equipment without causing additional damage to the equipment or injury to self.
3	Design, analyze and troubleshoot basic electronics circuits using a variety of test equipment and tools.
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**3 Assessment Cycle timeline for the above student learning outcomes for the next five years.**

<b>Outcome #</b>	<b>When Measured</b>	<b>Where measured (i.e. what course(s))</b>	<b>Measurement tool(s) &amp; Type of tool</b>
1	Final Semester	Capstone	Individual student performance
2	Final Semester	Capstone	Individual student performance
3	Final Semester	Capstone	Individual student performance
4			
5			
6			
7			
8			
9			
10			